

Atty. Dkt. No: SYN-0174

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Thomas W. Williams et al.

Application No.: 09/728,022

Art Unit.: 2133

Filing Date: 11/30/2000

Examiner: John J. Tabone Jr.

For: "Intelligent Test Vector Formatting To Reduce Test Vector Size And Allow Encryption Thereof For Integrated Circuit Testing"

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Date: April 20, 2005

**AMENDMENT TRANSMITTAL**

1. Transmitted herewith is an amendment for this application.
2. **STATUS:** Applicant is other than a small entity.
3. **EXTENSION OF TERM:** The proceedings herein are for a patent application and the provisions of 37 C.F.R. 1.136 apply. Applicant believes that no extension of term is required. However, this conditional petition is being made to provide for the possibility that applicant has inadvertently overlooked the need for a petition for extension of time.
4. **FEE FOR CLAIMS:** The fee for claims (37 C.F.R. 1.16(b)-(d)) has been calculated as shown below:

(Col.1)	(Col. 2)	(Col. 3)	LARGE ENTITY		
Claims Remaining After Amendment	Highest No. Previously Paid For	Present Extra	Rate	Addit. Fee	
Total 11	Minus 20	= 0	x \$50 =	\$0	
Indep. 2	Minus 3	= 0	x \$200 =	\$0	
First Presentation of Multiple Dependent Claim			+ \$360 =	\$0	
			Total		
			Addit. Fee	\$0	

**No additional fee for claims is required.**

5. **FEE DEFICIENCY:** If any additional extension and/or fee is required, please charge Deposit Account No. 50-0574.

  
**SIGNATURE OF PRACTITIONER**

Jeanette S. Harms

Reg. No. 35,537

Customer No. 35273

Tel.: (408) 451-5907

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as FIRST CLASS MAIL in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on April 20, 2005.

4/20/2005 Rebecca A. Baumann  
Date Signature: Rebecca A. Baumann

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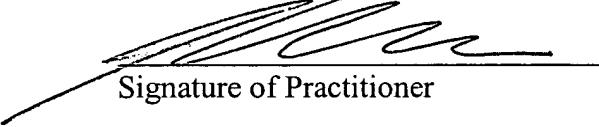
For: "INTELLIGENT TEST VECTOR FORMATTING TO REDUCE TEST VECTOR SIZE AND ALLOW ENCRYPTION THEREOF FOR INTEGRATED CIRCUIT TESTING"

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
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**SUBMISSION OF PROPOSED DRAWING AMENDMENT**  
**FOR APPROVAL BY EXAMINER**  
(37 C.F.R. section 1.123)

Attached please find Figure 2 showing the proposed changes to the drawing in this application, for which the approval of the Examiner is requested.

Date: April 20, 2005

  
Signature of Practitioner

Reg. No.: 35,537

Jeanette S. Harms

Tel. No.: (408) 451-5907

Bever, Hoffman & Harms, LLP

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